Amendments to the Specification

1.   Please replace paragraph [0011] with the following amended paragraph [0011]:

[0011]   FIG. 3 is a schematic diagram showing an example of an adaptive

initialization module in which the codec controller supports more than two codecs in

accordance with one embodiment of the present invention;

2.   Please replace paragraph [0016] with the following amended paragraph [0016]:

[0016]   A riser and method of configuring a plurality of riser codecs in accordance

with the present invention therefore provides a number of benefits. The riser includes a

plurality of riser codecs and an adaptive initialization module coupled to the riser codecs.

The initialization module configures the riser codecs when the riser is connected to a

motherboard having a codec controller and a primary codec. The initialization module

automatically selects between a first multi-codec configuration and a second multi-codec

configuration based on a codec support capability of the codec controller.

3.   Please replace paragraph [0017] with the following amended paragraph [0017]:

[0017]   In another aspect of the invention, an adaptive initialization module of a

riser includes a detection module and an address controller coupled to the detection

module.   The detection module monitors the status of a signal, where the address

controller selects a two-codec address structure when the signal status indicates that the

codec controller supports up to two codecs. The address controller selects a three-codec

address structure when the signal status indicates that the codec controller supports up to

three codecs. In one approach, the signal status indicates whether data is to be delivered

from a primary codec to the codec controller.